

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	874	"716"/\$.ccls. and (short\$ same path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 07:52
S2	652	"716"/\$.ccls. and (short\$ with path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 07:52
S3	28	"716"/\$.ccls. and (short\$ with path) and (lvs or (layout near2 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 08:12
S4	5	("6449753" "6507935").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 08:09
S5	41	"716"/\$.ccls. and (short\$ with (path or connection)) and (lvs or (layout near2 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 08:16
S6	36	"716"/\$.ccls. and (short\$ with (path or \$4connection)) and (lvs or (layout near2 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 08:55
S7	4	("5519627"   "5640327"   "5812417"   "6327697").PN.	USPAT	OR	ON	2004/07/08 08:22
S8	0	"6507932".URPN.	USPAT	OR	ON	2004/07/08 08:49
S9	6	("5903469"   "5999726"   "6009252"   "6099581"   "6128768"   "6243653").PN.	USPAT	OR	ON	2004/07/08 08:49
S10	46	"716"/\$.ccls. and (short\$ with (path or \$4connection or rout\$3)) and (lvs or (layout near2 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 09:20
S11	48	"716"/\$.ccls. and (short\$ near2 path) and (short near2 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/08 09:21

S12	5	indermaur.IN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:12
S13	14	("5247468"   "5765194"   "5815655"   "6053947"   "6077717"   "6094654"   "6249899"   "6260180"   "6275970"   "6279143"   "6295632"   "6301691"   "6308301"   "6311314").PN.	USPAT	OR	ON	2004/07/08 07:51
S14	322	((integrated or semiconductor or logic) adj circuit) and (schematic or layout or artwork) and (short same label) and (wire or interconnect\$3 or connect\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 10:30
S15	13	((integrated or semiconductor or logic) adj circuit) and ((schematic or layout or artwork or route) same (short same label)) and (wire or interconnect\$4 or connect\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/24 12:46
S18	5	((integrated or semiconductor or logic) adj circuit) and ((schematic or layout or artwork or route) same (short adj circuit)) and (wire or interconnect\$4 or connect\$5)) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 10:25
S19	2	((integrated or semiconductor or logic) adj circuit) and (schematic or layout or artwork) and (short same label) and (wire or interconnect\$3 or connect\$4) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 10:35
S20	12	((integrated or semiconductor or logic) adj circuit) and ((schematic or layout or artwork or route) same (short with circuit)) and (wire or interconnect\$4 or connect\$5) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 10:46
S21	1	((integrated or semiconductor or logic) adj circuit) and (short adj locator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 10:48
S22	0	(short adj locator)and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 12:27
S23	24	(short adj locator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/23 15:21

S24	2	(short with locator) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 16:46
S25	0	(short with locator) and lvs and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/28 16:47
S26	153	(short with circuit) and lvs and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 10:40
S27	128	(layout or route or (place near2 route)) and (copy with rename)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 10:48
S28	65	((layout or route or (place near2 route)) and (copy with rename)) and label	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 10:48
S29	2	"716"/\$.CCLS. and (copy with rename)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 12:56
S30	0	(connectivity adj extract) with tool	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 12:57
S31	1	(connectivity near extract) with tool	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 14:17
S32	189	(shortest adj path) and (short near3 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/01/29 14:20
S33	81	((location near3 short) or (short adj circuit adj locat\$3)) and circuit and schematic and copy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/05/21 12:24

S35	190	((integrated or semiconductor or logic) adj circuit) and ((schematic or layout or artwork or route) same (short with circuit)) and (wire or interconnect\$4 or connect\$5) and (name or identifier)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/02 14:54
S36	5	(short with circuit) and (infer\$4 with label)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/25 09:16
S37	17	(short with circuit) and (infer\$4 with (label or name))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/25 09:32
S38	1521	(schematic) and label and (short with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/31 10:24
S39	42	"716"/\$.CCLS. and (schematic) and label and (short with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/02/03 10:25
S40	1	"20020188924"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/11/03 15:57
S41	44	("5182718"   "5326659"   "5340700"   "5432714"   "5533148"   "5538815"   "5572598"   "5631110"   "5657235"   "5682323"   "5795688"   "5801954"   "5804340"   "5815685"   "5825647"   "5847959"   "5849440"   "5862058"   "5885734"   "5900338"   "5923566"   "6009250"   "6009251"   "6011911"   "6016357"   "6023328"   "6064806"   "6076465"   "6077310"   "6078738"   "6081658"   "6081659"   "6091845"   "6130750"   "6171731"   "6272236"   "6334209"   "6339836"   "6346426"   "6370677"   "6370679"   "6449749"   "6453452"   "6470489"   "2002/0010904"   "2002/0019729"   "2002/0152449"   "2002/0152454"). PN.	USPAT	OR	ON	2003/11/03 15:58
S42	12	skoll.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/02/03 11:02

S43	10	("4623255"   "5086477"   "5590271"   "5694150"   "5699534"   "5761064"   "6075531"   "6177937"   "6184883"   "6219027").PN.	USPAT	OR	ON	2004/02/03 10:42
S44	35	"716"/\$.ccls. and schematic and (short with locat\$) and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:19
S45	194	(location with short with electrical) and (schematic) and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/23 15:11
S46	10	("4733175"   "5307077"   "5424605"   "5432461"   "5463216"   "5730634"   "5751262"   "5816703"   "6046429"   "6111424").PN.	USPAT	OR	ON	2004/06/23 15:12
S47	10	("4733175"   "5307077"   "5424605"   "5432461"   "5463216"   "5730634"   "5751262"   "5816703"   "6046429"   "6111424").PN.	USPAT	OR	ON	2004/06/23 15:13
S48	17	("2243259"   "4110683"   "4446421"   "4568872"   "4605827"   "4656416"   "5087874"   "5162742"   "5299139"   "5359291"   "5432460"   "5485094"   "5539306"   "5682100"   "5701081"   "6219335"   "6242923"   "2002/0053912").PN.	USPAT	OR	ON	2004/06/23 15:14
S49	86	(short with circuit with locator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/23 15:32
S50	12	indermaur.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 09:13
S51	2219	(short with circuit) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:16
S52	177	(short with circuit with electrical) and lvs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:15

S53	2	"6470482".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:15
S55	25	( (short adj circuit) and lvs) and copy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 10:58
S56	1	(electrical adj short) and lvs and copy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:25
S57	5	(electric\$2 near2 short) and lvs and copy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:28
S58	7	(electric\$2 near2 short) and (layout with schematic) and copy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 10:30
S59	86	(electric\$2 near2 short) and (layout with schematic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:42
S60	0	(shortest adj electrical adj path) and (layout with schematic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:42
S61	111	(shortest near2 path) and (layout with schematic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:48
S62	0	(shortest near2 path) with (lvs)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:49
S63	0	(shortest near2 path) same (lvs)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:49

S64	130	(shortest near2 path) and (lvs)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 12:49
S65	255	lvs with short	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:23
S66	4	(lvs with short) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:13
S67	13	skoll.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:20
S68	0	lvs same (shortest adj path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:23
S69	98	lvs and (shortest adj path)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/07 13:24
S10 1	2	"20040098687"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 14:50